

CLAIMS:

We claim:

- 5 1. An adapter comprising:
 - (a) a first and a second surface;
 - (b) at least one first interconnect on the first surface,
 - (c) at least one second interconnect on the second surface;
 - (d) at least one connective path between the first and second interconnects, and
 - 10 (e) a signal modifying circuit between the first interconnect and the second interconnect.
2. The adapter of Claim 1 wherein the first set of interconnects are physically spaced to correspond to a first pin configuration of a power module.
- 15 3. The adapter of Claim 1 wherein the second set of interconnects are physically spaced to correspond to a second pin configuration of an end user's circuit board.
4. The adapter of Claim 1 wherein a signal modifying circuit acts upon an input to the adapter.
- 20 5. The adapter of Claim 1 wherein a signal modifying circuit acts upon an output to the adapter.
6. The adapter of Claim 2 wherein the power module is a DC-to-DC converter.
- 25 7. The adapter of Claim 2 wherein the power module is an AC-to-DC inverter.
8. The adapter of Claim 2 wherein the power module is a DC-to-AC inverter.
- 30 9. The adapter of Claim 1 wherein the first interconnects comprise surface mount connects.

10. The adapter of Claim 1 wherein the first interconnects comprise through hole connects.

11. The adapter of Claim 1 wherein the second interconnects comprise surface mount connects.

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12. The adapter of Claim 1 wherein the second interconnects comprise through hole connects.

13. The adapter of Claim 1 wherein the second interconnects comprise a filter.

10 14. The adapter of Claim 1 wherein the second interconnects comprise an overvoltage protection device.

Patent Application